

CHAITANYA

(Deemed to be University) Ranga Reddy Dist - Hyderabad - (T.S.)

B.TECH FIRST SEMESTER I-Internal EXAMINATIONS, Nov - 2024

Theory TIME TABLE

TIME: 11.30 AM to 01.00 PM

DATE	B.Tech (CE)	B.Tech (CSE) A, A1	B.Tech (ECE)	B.Tech (EEE)	B.Tech (ME)	B.Tech (CSE) AI & ML	B.Tech (CSE) DS
18-11-2024 MON	Concepts in Physics for Engineering	Concepts in Physics for Engineering	Concepts in Physics for Engineering	Concepts in Physics for Engineering	Concepts in Physics for Engineering	Concepts in Chemsitry for Engineering	Concepts in Physics for Engineering
19-11-2024 TUE	Mathematics - I (Calculus&Linear Algebra)	Mathematics - I (Calculus&Linear Algebra)	Mathematics - I (Calculus&Linear Algebra)				
20-11-2024 WED	Basic Electrical Engineering	Basic Electrical Engineering	Basic Electrical Engineering	Basic Electrical Engineering	Basic Electrical Engineering	Programming for Problem Solving	Basic Electrical Engineering
21-11-2024 THU	Engineering Graphics &Design	Engineering Graphics &Design	Engineering Graphics &Design	Engineering Graphics &Design	Engineering Graphics &Design	Engineering Graphics &Design	Engineering Graphics &Design
22-11-2024 FRI	Universal Human Values - II Understanding	Universal Human Values - II Understanding	Universal Human Values - II Understanding	Universal Human Values - II Understanding			

Note: The Classes will run as usual after the Internal examination



CONTROLLER OF EXAMINATIONS